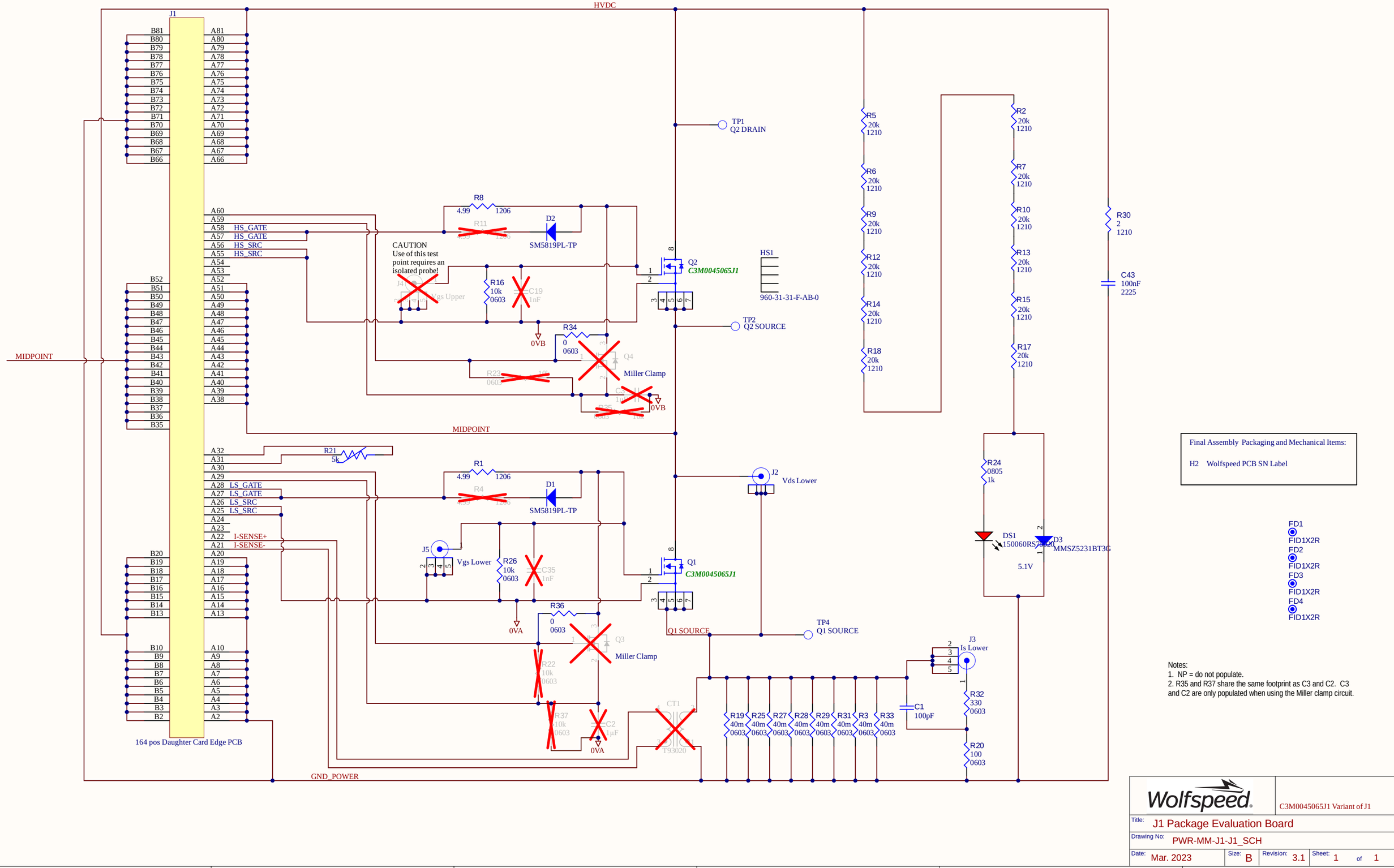
HVDC
J1
164 pos Daughter Card Edge PCB
MIDPOINT
GND_POWER
0VA
0VB
R8
4.99
1206
R11
D2
SM5819PL-TP
CAUTION
Use of this test point requires an isolated probe!
J4
Vgs Upper
R16
10k
0603
C19
1nF
R34
0
0603
Q4
Miller Clamp
R23
0603
C3
1nF
R35
0603
TP1
Q2 DRAIN
Q2
C3M0045065J1
HS1
960-31-31-F-AB-0
TP2
Q2 SOURCE
R5
20k
1210
R6
20k
1210
R9
20k
1210
R12
20k
1210
R14
20k
1210
R18
20k
1210
R2
20k
1210
R7
20k
1210
R10
20k
1210
R13
20k
1210
R15
20k
1210
R17
20k
1210
R30
2
1210
C43
100nF
2225
R21
5k
R1
4.99
1206
R4
D1
SM5819PL-TP
J2
Vds Lower
J5
Vgs Lower
R26
10k
0603
C35
1nF
Q1
C3M0045065J1
R36
0
0603
Q3
Miller Clamp
R22
10k
0603
R37
10k
0603
C2
1µF
CT1
T93020
Q1 SOURCE
TP4
Q1 SOURCE
R19
40m
0603
R25
40m
0603
R27
40m
0603
R28
40m
0603
R29
40m
0603
R31
40m
0603
R3
40m
0603
R33
40m
0603
C1
100pF
J3
Is Lower
R32
330
0603
R20
100
0603
R24
0805
1k
DS1
150060RS75000
D3
MMSZ5231BT3G
5.1V
FD1
FID1X2R
FD2
FID1X2R
FD3
FID1X2R
FD4
FID1X2R
Final Assembly Packaging and Mechanical Items:
H2 Wolfspeed PCB SN Label
Notes:
1. NP = do not populate.
2. R35 and R37 share the same footprint as C3 and C2. C3 and C2 are only populated when using the Miller clamp circuit.
Wolfspeed
C3M0045065J1 Variant of J1
Title: J1 Package Evaluation Board
Drawing No: PWR-MM-J1-J1_SCH
Date: Mar. 2023
Size: B
Revision: 3.1
Sheet: 1 of 1